AMENDMENTS TO THE CLAIMS

Claim 1 (currently amended): A method, comprising the steps of:

detecting that an operation on a control and status register/remote monitor ~~and~~ counter (CSR/RMON) block is needed;

enabling a clock signal to the ~~register and counter~~ CSR/RMON block; and

executing the operation on the ~~register and counter~~ CSR/RMON block through employment of the clock signal.

Claim 2 (currently amended): The method of claim 1, further comprising the step of:

disabling the clock signal to the ~~register and counter~~ CSR/RMON block after execution of the operation.

Claim 3 (currently amended): The method of claim 1, wherein the executing step comprises the step of:

programming a control register in the ~~register and counter~~ CSR/RMON block.

Claim 4 (currently amended): The method of claim 1, wherein the ~~register and counter~~ CSR/RMON block is in a media access control (MAC) component.

Claim 5 (original): The method of claim 4, wherein the detecting step comprises the step of:

detecting an interrupt signal from the media access control component.

Claim 6 (original): The method of claim 5, wherein the enabling step comprises employing the interrupt signal to enable the clock signal.

Claim 7 (currently amended): The method of claim 1, wherein the executing step comprises:

reading at least one of a remote monitor ~~(RMON)~~ counter, a control register, and a status register in the ~~register and counter~~ CSR/RMON block.

Claim 8 (currently amended): A method for reading one of a status register and a remote monitor counter in a control and status register/remote monitor ~~and~~ counter (CSR/RMON) block ~~a storage component~~ in a media access control component comprising the steps of:

detecting an update to the one of the status register and the remote monitor counter ~~storage component~~;

providing a clock signal to the one of the status register and the remote monitor counter ~~storage component~~ in response to detection of the update; and

reading the one of the status register and the remote monitor counter ~~storage component~~ through employment of the clock signal.

Claim 9 (cancelled)

Claim 10 (currently amended): The method of claim 8, further comprising the step of:

disabling the clock signal after the one of the status register and the remote monitor counter ~~storage component~~ has been read.

Claim 11 (currently amended): A method for programming a ~~storage component~~ control register in a control and status register/remote monitor counter (CSR/RMON) block in a media access control component comprising:

determining that the ~~storage component~~ control register needs to be programmed;

providing a clock signal to the ~~control register~~ control register in response to a determination that the ~~storage component~~ control register needs to be programmed; and

programming the ~~storage component~~ control register through employment of the clock signal.

Claim 12 (currently amended): The method of claim 11, further comprising:

disabling the clock signal after the ~~storage component~~ control register has been programmed.

Claim 13 (cancelled)

Claim 14 (currently amended): A system comprising:

a detection unit that detects that an operation on a ~~register and counter~~ control and status register/remote monitor counter (CSR/RMON) block is needed;

a clock enable unit that enables a clock signal to the ~~register and counter~~ CSR/RMON block in response to a detection that the operation is needed; and

application logic that executes the operation on the ~~register and counter~~ CSR/RMON block through employment of the clock signal.

Claim 15 (currently amended): The system of claim 14, further comprising:

a clock disable unit that disables the clock signal to the ~~register and counter~~ CSR/RMON block after execution of the operation.

Claim 16 (currently amended): The system of claim 14, wherein the application logic comprises:

a control register program unit that programs a control register in the ~~register and counter~~ CSR/RMON block

Claim 17 (currently amended): The system of claim 14, wherein the ~~register and counter~~ CSR/RMON block is part of a media access control component.

Claim 18 (original): The system of claim 17, wherein the detection unit comprises:

a logic component that detects an interrupt signal from the media access control component.

Claim 19 (original): The system of claim 18, wherein the clock enable unit comprises a logic component that employs the interrupt signal to enable the clock signal.

Claim 20 (currently amended): The system of claim 14, wherein the application comprises:

at least one of a status register read unit that reads a status register in the CSR/RMON block; and

a remote monitor counter read unit that reads a remote monitor (RMON) counter in the CSR/RMON block.

Claim 21 (currently amended): A system for performing an operation on a control and status register/remote monitor counter (CSR/RMON) block in a media access control component comprising:

clock gating logic that detects that an operation on the CSR/RMON block ~~storage component~~ is to be performed and enables a clock signal to the CSR/RMON block ~~storage component~~ in response to a detection that an operation is to be performed; and

application logic that performs the operation on the CSR/RMON block ~~storage component~~ through employment of the clock signal.

Claim 22 (cancelled)

Claim 23 (currently amended): The system of claim 21 ~~20~~, wherein the clock gating logic disables the clock signal after the operation has been performed.